

Optical Measurement Engine (11) is connected to A/D Converter (12) which is connected to Preprocessor (13) which is connected to Correction (14) which is connected to Analyzer (15) which is connected to Display (16).

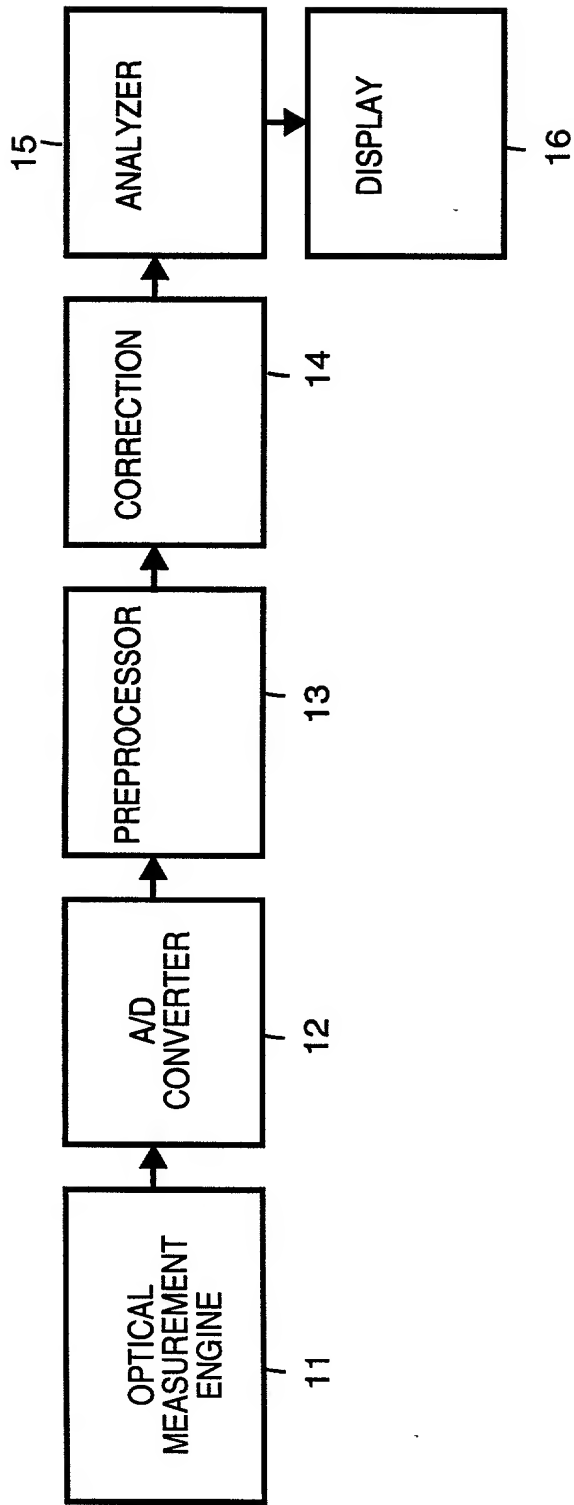
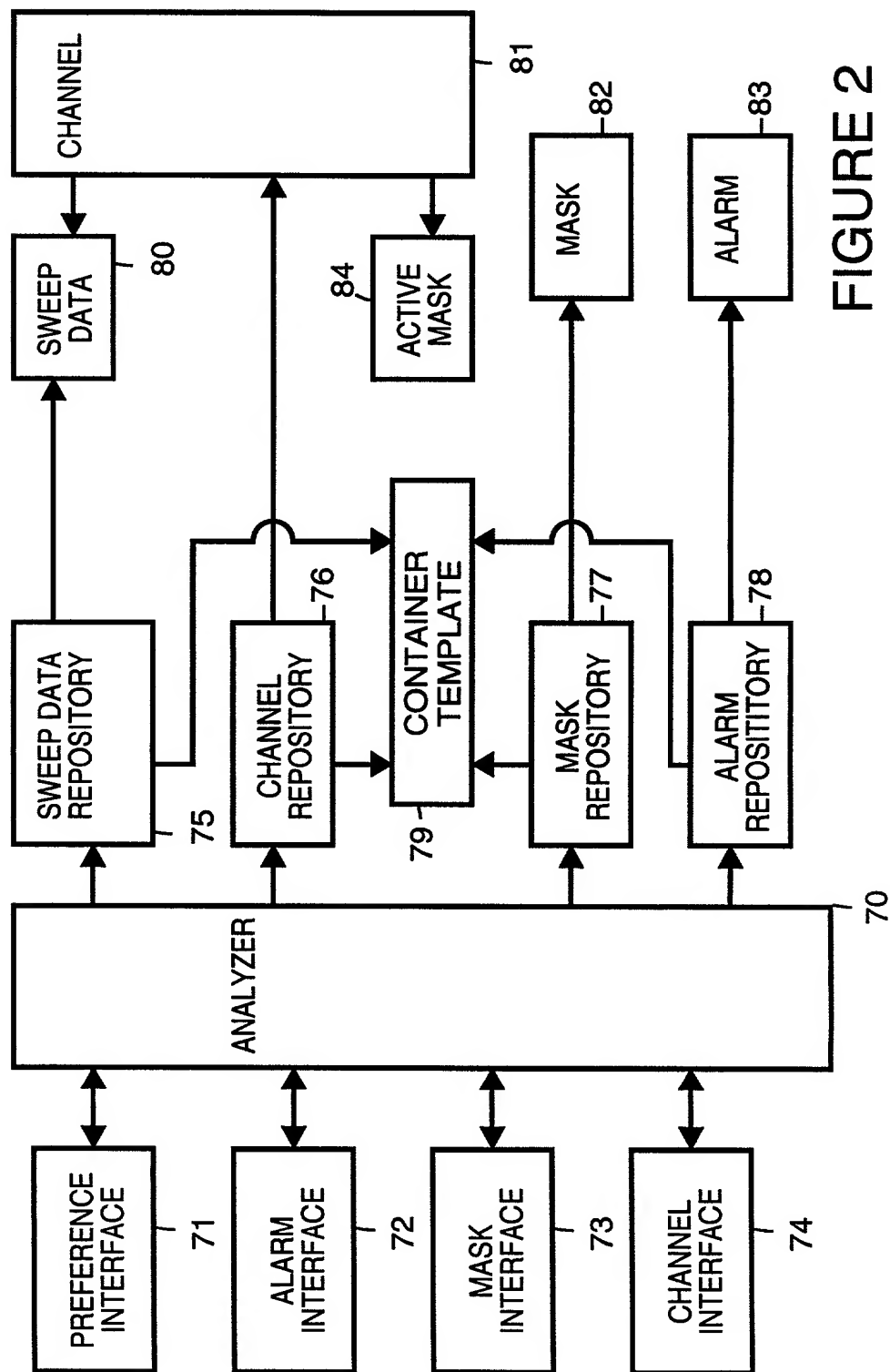


FIGURE 1



# FIGURE 2

FIG. 3 is a schematic diagram of a prior art device. The device includes a substrate 31 and a series of conductive lines 32. The lines are arranged in a periodic pattern, with each line having a central portion that is wider than its ends. The lines are connected to a common terminal 33. The device is shown in a perspective view, with the substrate 31 and the lines 32 being the primary components. The terminal 33 is located at one end of the device. The lines 32 are arranged in a row, with each line having a central portion that is wider than its ends. The lines are connected to a common terminal 33. The device is shown in a perspective view, with the substrate 31 and the lines 32 being the primary components. The terminal 33 is located at one end of the device.

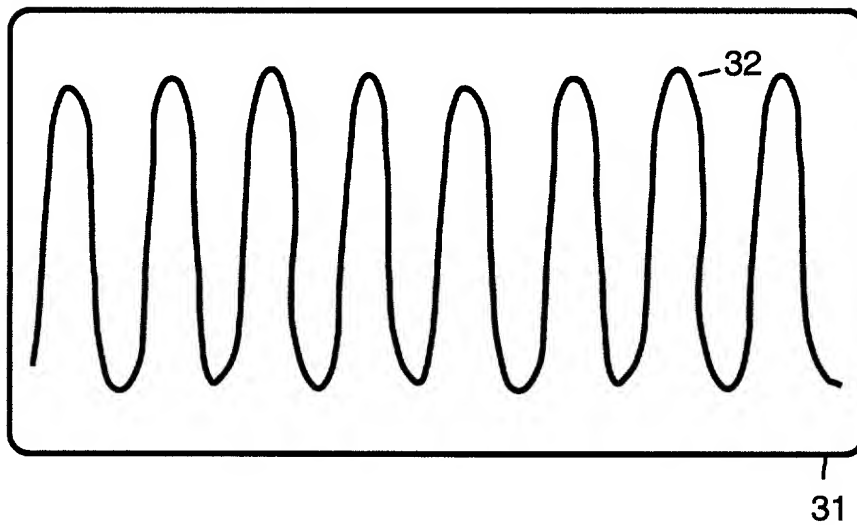


FIGURE 3 (PRIOR ART)

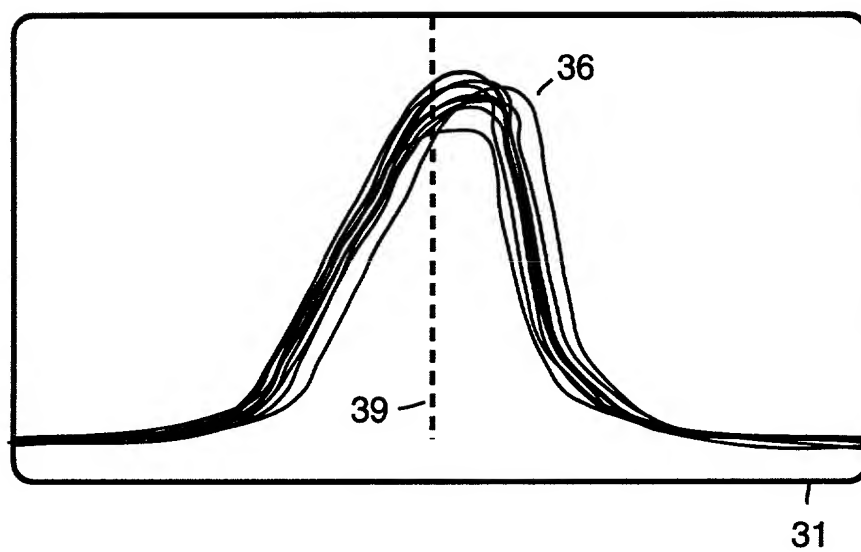


FIGURE 4



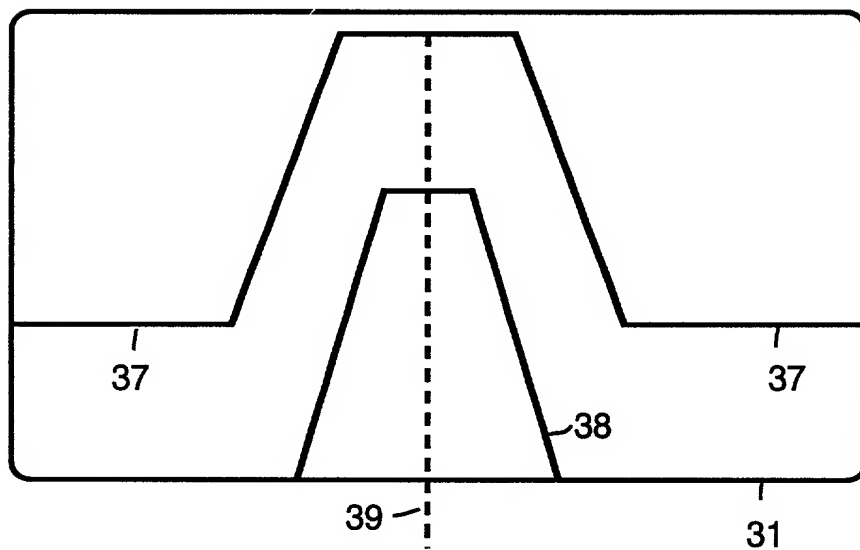


FIGURE 6

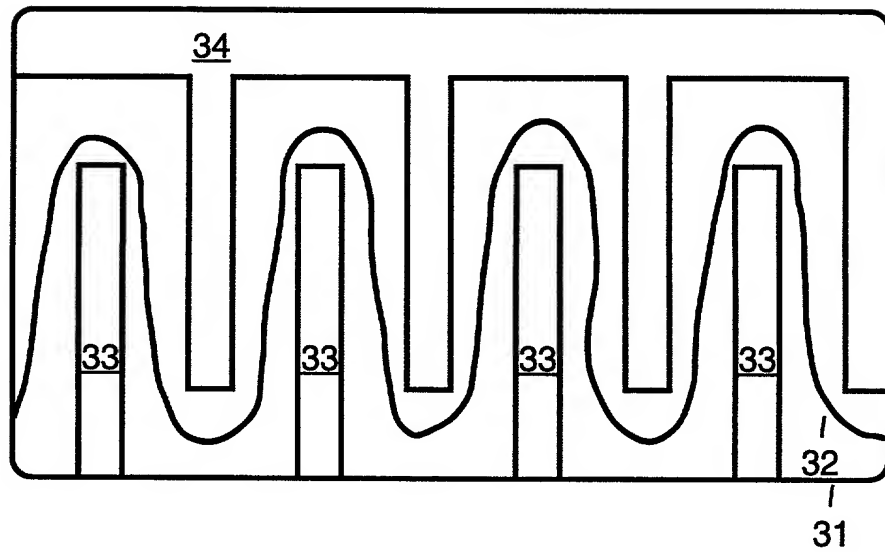


FIGURE 7

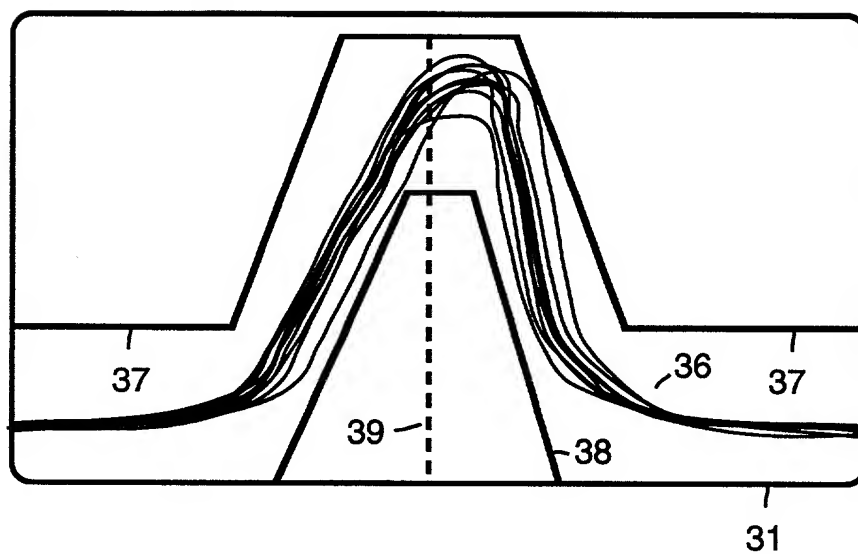


FIGURE 8